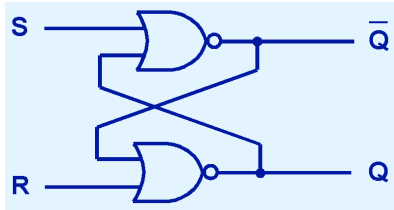


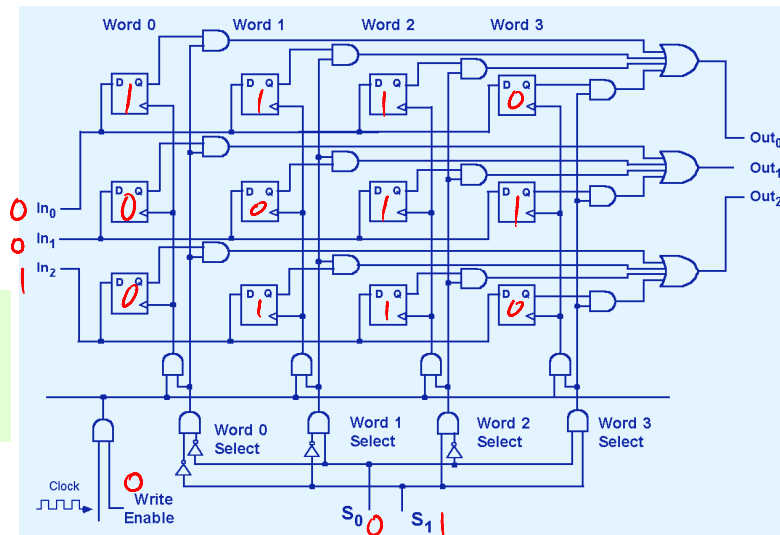
Puzzles for sequential circuits from Null & Lobur Chapter 3 powerpoint slides

(1) SR flip-flop



Present State			Next State
S	R	Q(t)	Q(t+1)
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	

(2) 4-word memory with 3-bit words



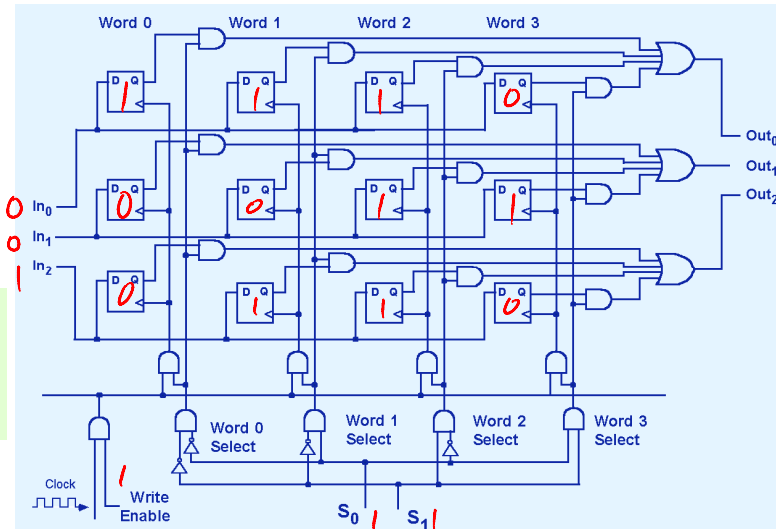
What are outputs for given states?

0  
0  
1

S<sub>0</sub> 0  
S<sub>1</sub> 1

### (3) 4-word memory with 3-bit words, again

What are outputs for given states?



### (4) 4-bit counter

Compute output for 8 clock cycles starting from given states

