Class	Day	Date	Topic	Reading required	work due
1	Mon	8/29	Introduction and motivation	-	
2	Thu	9/1	Unsigned whole numbers, and character codes	2.1, 2.2, 2.3.0, 2.3.1, 2.6.3, 2.6.4	
3	Mon	9/5	Signed whole numbers	2.4.0, 2.4.1, 2.4.2 (but skip "One's Complement")	
4	Thu	9/8	Floating-point numbers	2.5	HW1
5	Mon	9/12	History and structure of computers; discussion of WA1	1.1, 1.2, 1.3, 1.5.6, 1.6, 1.8	
6	Thu	9/15	Boolean algebra and logic gates	3.2.0, 3.2.1, 3.3, 3.4	
7	Mon	9/19	Combinational circuits	3.5	HW2
8	Thu	9/22	WA1 peer-review	bring printout of WA1a	WA1a
9	Mon	9/26	Sequential circuits	3.6.0-3, 3.6.5	
10	Thu	9/29	Hardware overview	4.0-4.7	
11	Mon	10/3	Assembly language introduction	4.8-4.10	HW3
12	Thu	10/6	Assembly language and assemblers	4.11	WA1b
13	Mon	10/10	**Exam 1 (covers classes 1-10)**		
14	Thu	10/13	Subroutines in assembly language	4.12	HW4
15	Mon	10/17	[Fall Pause]		
16	Thu	10/20	Assembly language practice + discussion of WA2	-	
17	Mon	10/24	Real-world architectures	4.14.0, 4.14.1	HW5
18	Thu	10/27	instruction set design	5.1-5.4	
19	Mon	10/31	instruction level pipelining and real-world ISAs	5.5, 5.6.1, 5.6.3	WA2
20	Thu	11/3	memory systems and performance	6.1-6.3	HW6
21	Mon	11/7	cache memory	6.4	
22	Thu	11/10	virtual memory	6.5.0-6.5.3	HW7
23	Mon	11/14	exam revision		
24	Thu	11/17	**Exam 2 (covers classes 11-21)**		
25	Mon	11/21	I/O systems	7.1-7.4.1	HW8
26	Thu	11/24	[Thanksgiving]		
27	Mon	11/28	disk systems	7.6.0, 7.6.1-2, 7.9.0, 7.9.2, 7.9.6	
28	Thu	12/1	hardware design project	tecs-ch-1, tecs-appendix-A	HW9
29	Mon	12/5	hardware design project + exam revision	--	
30	Thu	12/8	hardware design project + exam revision	--	HW10
	Thu	12/15	**Final exam, 2pm**		